ABSTRACT

A network access server (NAS) for use in a communications system utilized by users of telephone, fax, or modem calls, the NAS having more than one processor with a plurality of processors forming a module, each processor for performing various functions by executing code stored in memory device accessible by the processor wherein either upgrading of the stored code or recovery of one or more of the processors is required. Upon determining that one or more processors are 'bad', the NAS and schedules recovery of the 'bad' processor during a window of time defined by a predetermined duration of time in the day during which the communications system is least utilized by the users, disables the particular module so that no calls are processed through the particular module and attempts recovery of the 'bad' processor during the window of time.

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